



PTO/SB/08b (08-03)

Approved for use through 06/30/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

SEP 08 2003

Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

Sheet 2 of 2

<p>Substitute for form 1449B/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i></p>				<p align="center">Complete if Known</p> <table border="1"> <tr> <td>Application Number</td> <td>10/046,937</td> </tr> <tr> <td>Filing Date</td> <td>01/14/02</td> </tr> <tr> <td>First Named Inventor</td> <td>Jeff C. KLEIN et al.</td> </tr> <tr> <td>Art Unit</td> <td>2819</td> </tr> <tr> <td>Examiner Name</td> <td>Unknown</td> </tr> </table> <p align="center">Attorney Docket Number</p> <p>H0002065</p>		Application Number	10/046,937	Filing Date	01/14/02	First Named Inventor	Jeff C. KLEIN et al.	Art Unit	2819	Examiner Name	Unknown
Application Number	10/046,937														
Filing Date	01/14/02														
First Named Inventor	Jeff C. KLEIN et al.														
Art Unit	2819														
Examiner Name	Unknown														
Sheet	2	of	2												

Examiner Signature  Date Considered 10/14/2001

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**INFORMATION DISCLOSURE
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Complete if Known

Application Number	10/048,937
Filing Date	01/14/02
First Named Inventor	Jeff C. KLEIN et al.
Art Unit	2819
Examiner Name	Unknown
Attorney Docket Number	H0002065

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				
Sheet	2	of	2	Attorney Docket Number
		H0002065		

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Implementation Technology, Chapter 3, April 5, 1999, Pages 61-100.	
		XESS CORPORATION, "What are CPLDs and FPGAs?", http://www.xess.com/fpgatut.htm , Date visited: 3/20/02, 3 pages.	
		RICHARD GEISSLER, "1.2.1 Basics of Synthesis and Optimization", http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node10.html , Date visited: 3/20/02, 2 pages.	
		RICHARD GEISSLER, "1.2.2 Logic-Level Optimization", http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node11.html , Date visited: 3/20/02, 3 pages.	
		RICHARD GEISSLER, "1.3 Synthesis Design Flow", http://mikro.e-technik.uni-ulm.de/vhdl/anl-engl.syn/html/node12.html , Date visited: 3/20/02, 3 pages.	
		Language Overview, "What is VHDL", www.acc-edu.com/vhdlref/refguide/language_overview/language_overview.htm , Date visited: 12/26/01, 3 pages.	
		Language Overview, "Test Benches", www.acc-edu.com/vhdlref/refguide/language_overview/test_benches/test_benches.htm , Date visited: 12/26/01, 11 pages.	

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10/14/2002

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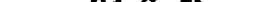
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Substitute for form 1449A/PTO				<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/046,937
				Filing Date	January 14, 2002
				First Named Inventor	Jeff C. Klein
				Group Art Artist	
				Examiner Name	
Sheet	'1	of	2	Attorney Docket Number	H0002065

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	10/14/2004
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